

2- JAGUAR JK-99

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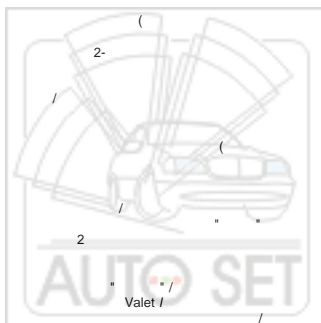
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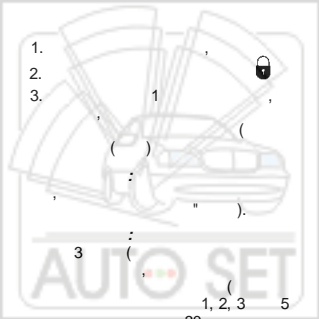
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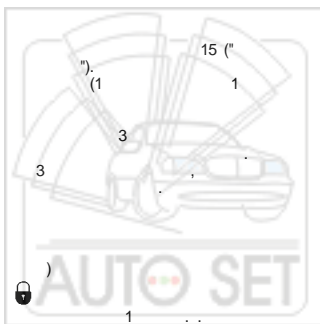
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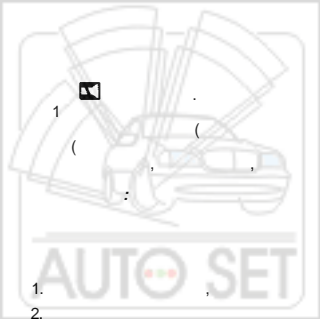
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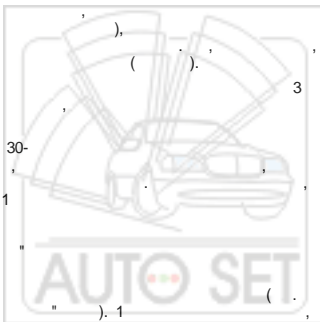
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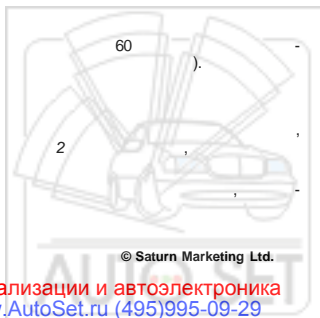
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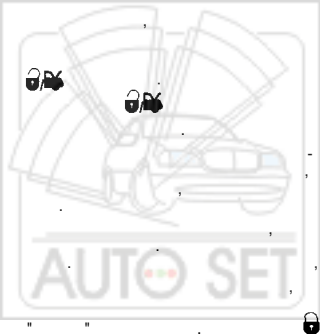
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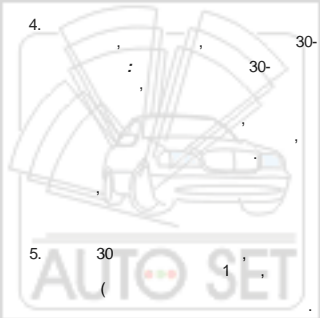
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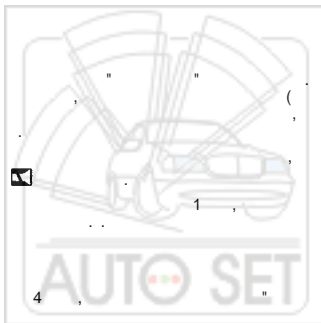
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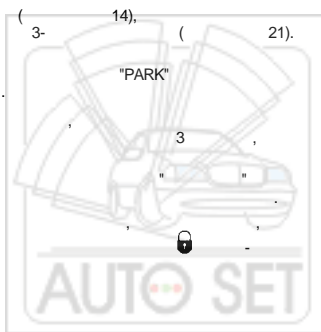
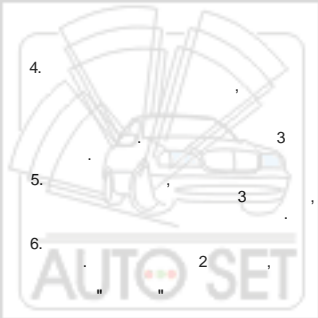
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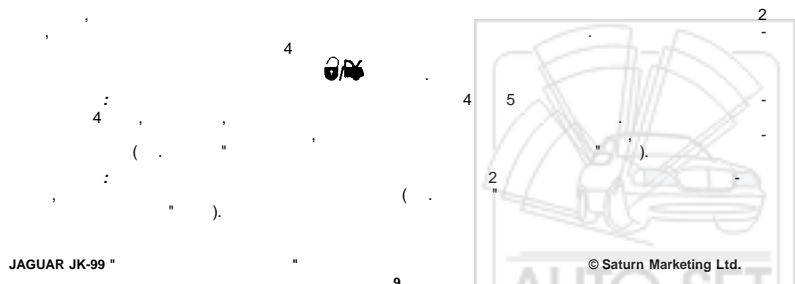
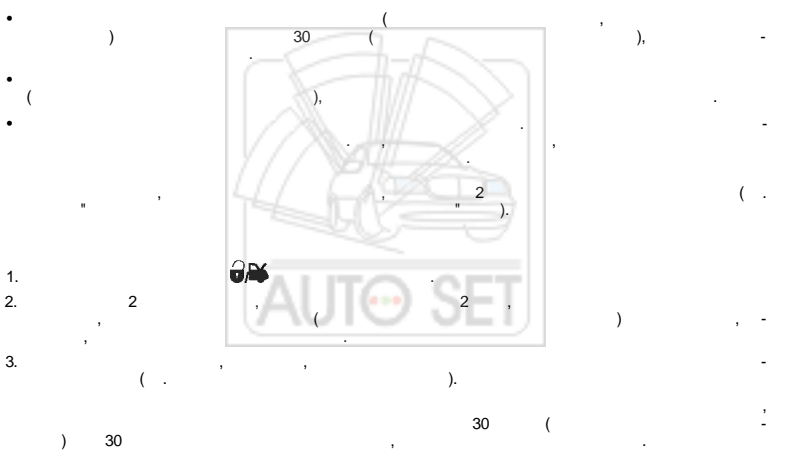
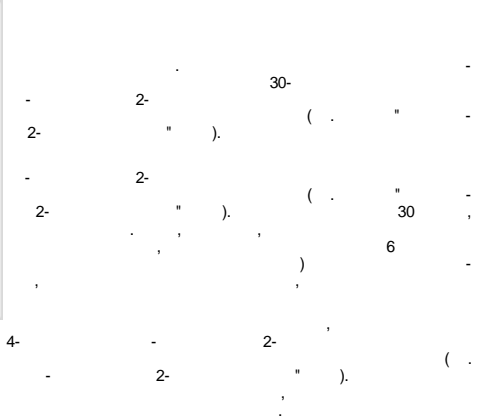
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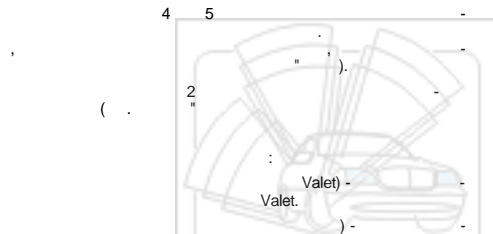
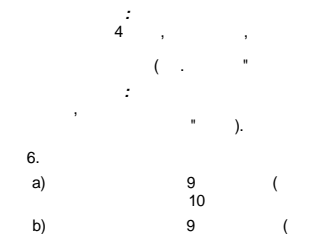
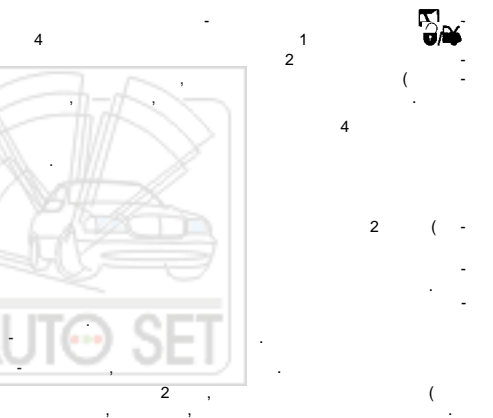
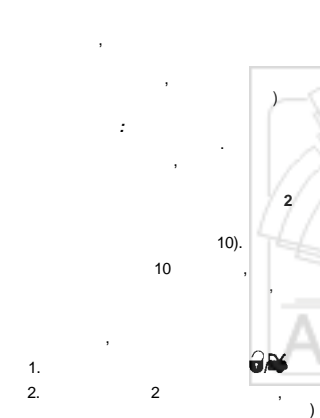
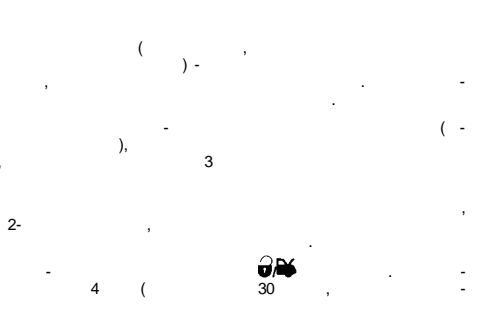
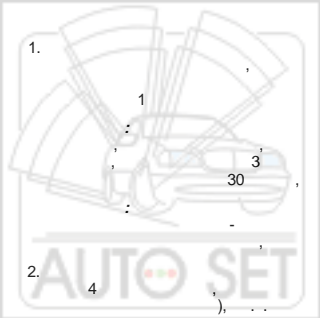


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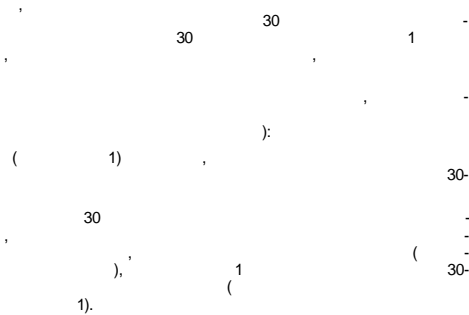
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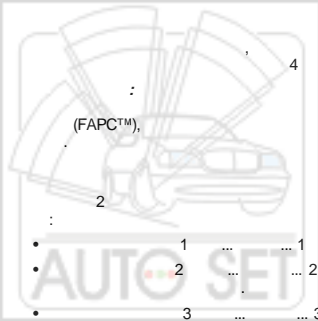
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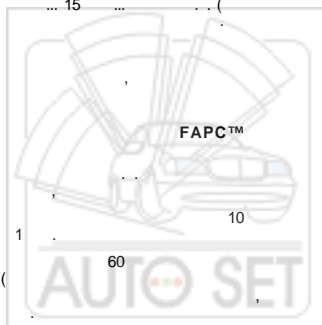
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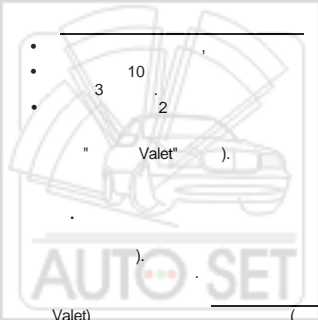
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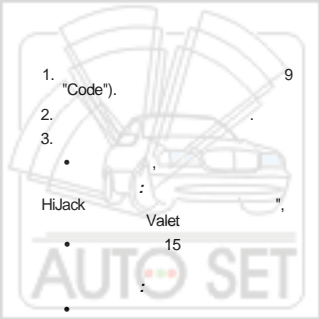
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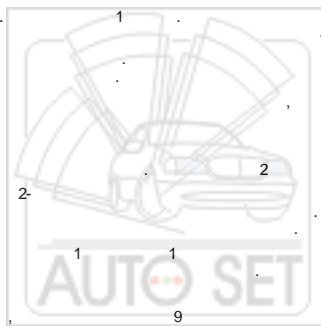
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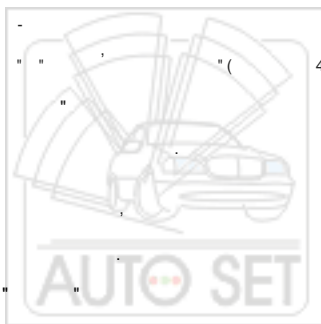
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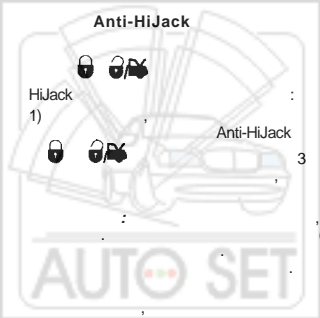
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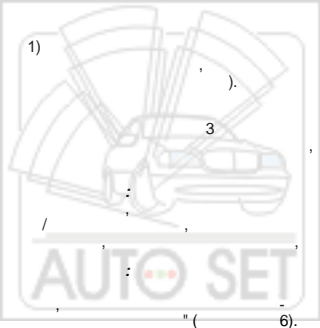
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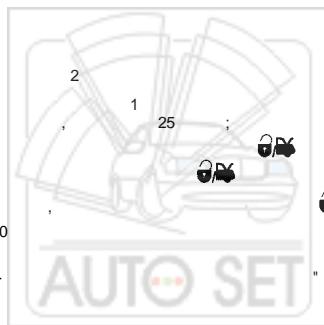
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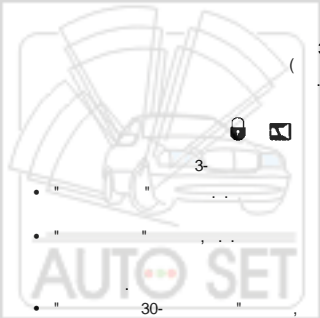
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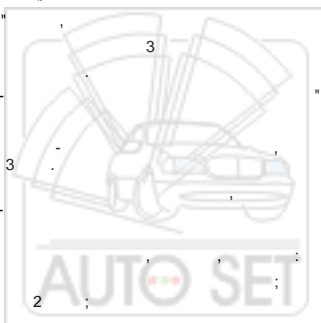
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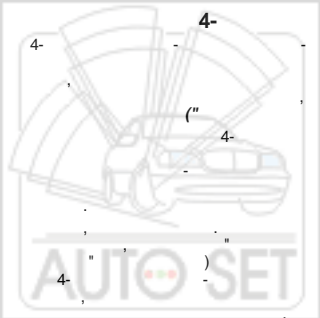
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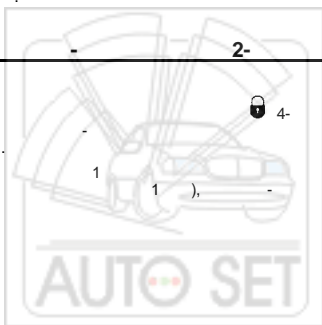


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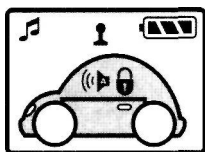
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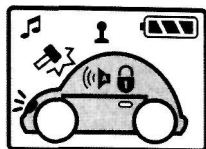
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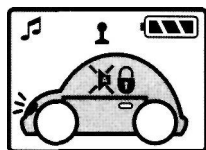
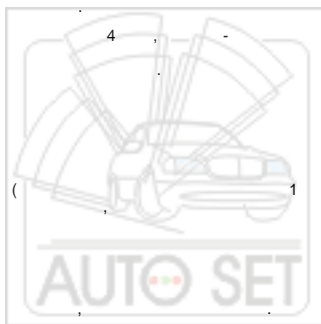
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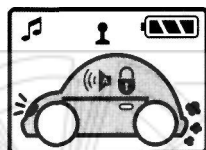
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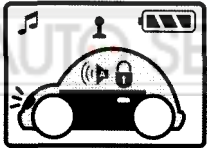
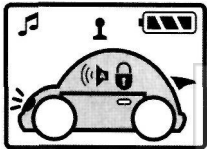
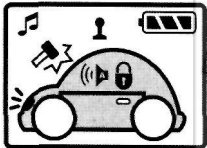
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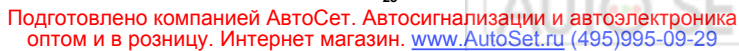


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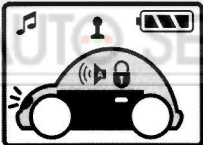
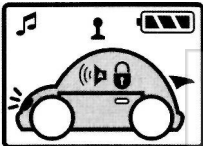
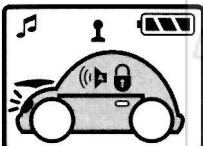
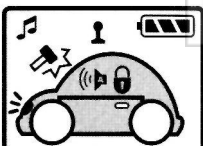

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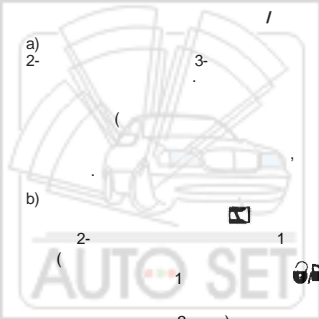
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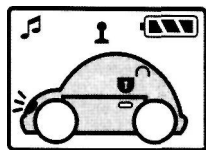
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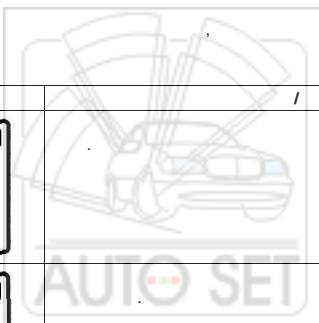


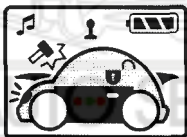
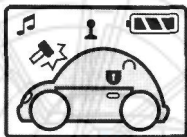
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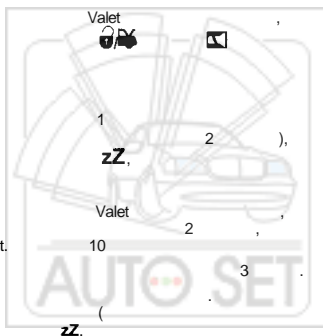
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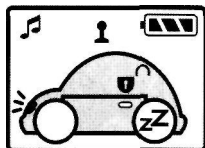
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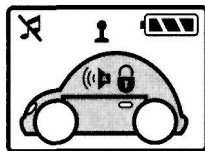
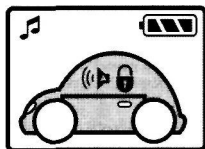
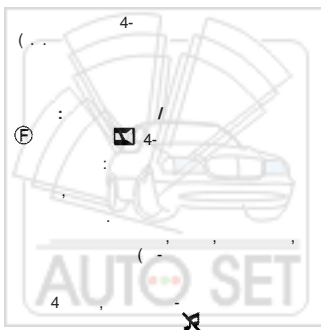
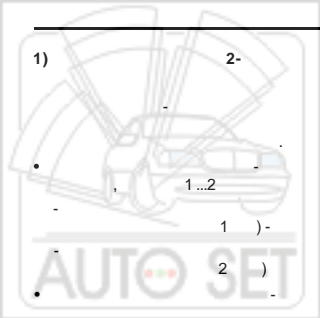
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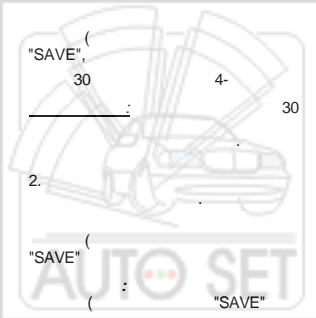
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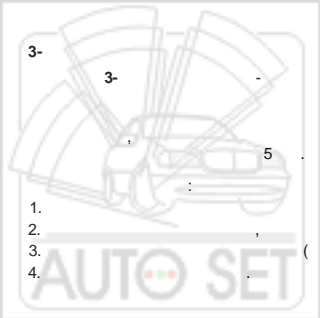


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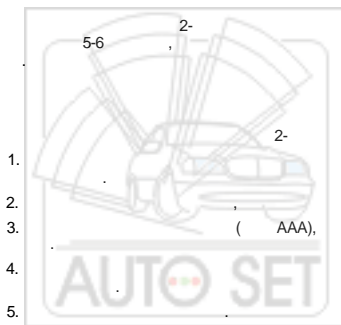
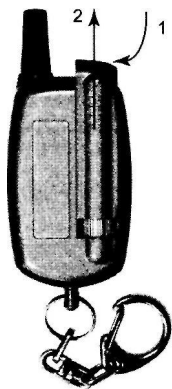
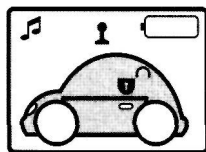
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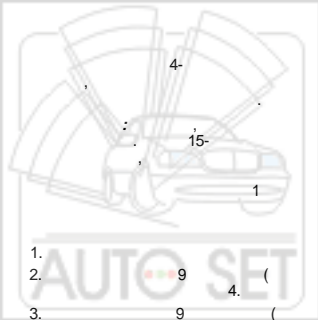


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"11"

HiJack

Valet

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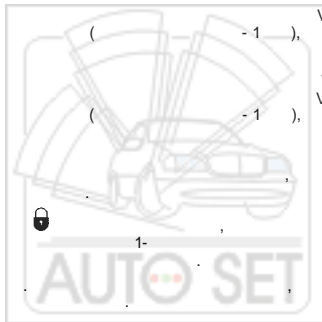
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Anti-HiJack,



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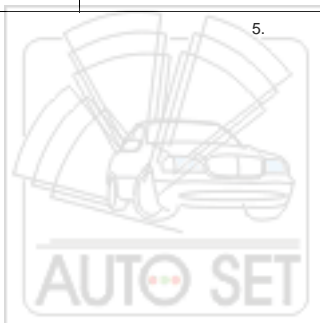
Diagram of a 4-bit shift register. The register is labeled "AUTO SET". It has four inputs labeled 1, 2, 3, and 4. It has four outputs labeled 5, 6, 7, and 8. The register is shown as a horizontal bar with four segments. The first segment is labeled 1, the second 2, the third 3, and the fourth 4. The outputs are labeled 5, 6, 7, and 8. The register is shown in a state where the first segment is 1, the second is 2, the third is 3, and the fourth is 4. The outputs are 5, 6, 7, and 8.

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